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ISSUE CLASSIFICATION	
Class	Subclass

**U.S. UTILITY Patent Application**

O.I.P.E.	PATENT DATE
SCANNED <i>IR</i> <i>PC4</i> Q.A. <i>LT</i>	

APPLICATION NO. 09/764711	CONT/PRIOR D.	CLASS 25 428	SUBCLASS 48	ART UNIT 28n	EXAMINER Roman
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## APPLICANTS

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**TITLE**

# Method of depositing silicon with high step coverage

PTO-2040  
12/99

**ISSUING CLASSIFICATION**

[illegible]

<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b>			<b>CLAIMS ALLOWED</b>	
	Sheets Drwg. _____ (Assistant Examiner) _____ (Date) _____	Figs. Drwg. _____ (Assistant Examiner) _____ (Date) _____	Print Fig. _____ (Assistant Examiner) _____ (Date) _____	Total Claims _____	Print Claim for O.G. _____
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.  <input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent. No. _____ _____ _____	_____ (Assistant Examiner) _____ (Date) _____  _____ (Primary Examiner) _____ (Date) _____  _____ (Legal Instruments Examiner) _____ (Date) _____			<b>NOTICE OF ALLOWANCE MAILED</b>	
				<b>ISSUE FEE</b>	
				Amount Due _____	Date Paid _____
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.				<b>ISSUE BATCH NUMBER</b>	
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